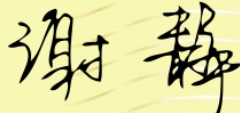



ENA Engineering Recommendation G83

Issue 2 2012

Type Verification Test Report

| | | | |
|---|---|--|--|
| Type Approval and manufacturer/supplier declaration of compliance with the requirements of Engineering Recommendation G83/2. | | | |
| SSEG Type reference number | | 2015093001 | |
| SSEG Type | | GW3000D-NS/GW3600-NS/GW3600D-NS | |
| System Supplier name | | Jiangsu GoodWe Power Supply Technology Co.,Ltd. | |
| Address | | NO.189 Kun Lun Shan Road, Suzhou New District, Jiangsu,china | |
| Tel | +86 512 6239 7998 | Fax | +86 512 6239 7972 |
| E:mail | service@goodwe.com.cn | Web site | http://www.goodwe.com.cn |
| Maximum rated capacity(use separate sheet if more than one connection option) | Connection Option | | |
| | 3 | kW single phase | |
| | 3.68 | kW single phase | |
| | 3.68 | kW single phase | |
| <p>SSEG manufacturer/supplier declaration.</p> <p>I certify on behalf of the company named above as a manufacturer/supplier of Small Scale Embedded Generators, that all products manufactured/supplied by the company with the above SSEG Type reference number will be manufactured and tested to ensure that they perform as stated in this Type Verification Test Report, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of G83/2.</p> | | | |
| Signed |  Xie Jing | On behalf of |  Huang min |

| Power Quality, Harmonics | | | | | | P |
|---|-----------------------------|-------------------------------|-----------------------------|-------------------------------|--------------------------------|---|
| The requirement is specified in section 5.4.1, test procedure in Annex A or B 1.4.1 | | | | | | |
| SSEG rating per phase (rpp) | | | | NV=MV*3.68/rpp | | |
| At 45-55% of rated output kW | | 100% of rated output kW | | | | |
| Harmonic | Measured Value (MV) in Amps | Normalized Value (NV) in Amps | Measured Value (MV) in Amps | Normalized Value (NV) in Amps | Limit inBS EN61000-3-2 in Amps | Higher limit for odd harmonics 21 and above |
| 2nd | 0.05 | 0.087 | 0.075 | 0.116 | 1.080 | |
| 3rd | 0.091 | 0.125 | 0.138 | 0.157 | 2.300 | |
| 4th | 0.014 | 0.031 | 0.008 | 0.031 | 0.430 | |
| 5th | 0.035 | 0.049 | 0.01 | 0.034 | 1.140 | |
| 6th | 0.022 | 0.041 | 0.002 | 0.037 | 0.300 | |
| 7th | 0.035 | 0.069 | 0.003 | 0.056 | 0.770 | |
| 8th | 0.012 | 0.04 | 0.02 | 0.061 | 0.230 | |
| 9th | 0.003 | 0.059 | 0.029 | 0.071 | 0.400 | |
| 10th | 0.011 | 0.061 | 0.011 | 0.043 | 0.184 | |
| 11th | 0.053 | 0.079 | 0.019 | 0.076 | 0.330 | |
| 12th | 0.017 | 0.047 | 0.018 | 0.052 | 0.153 | |
| 13th | 0.036 | 0.082 | 0.014 | 0.056 | 0.210 | |
| 14th | 0.01 | 0.051 | 0.028 | 0.053 | 0.131 | |
| 15th | 0.028 | 0.061 | 0.03 | 0.076 | 0.150 | |
| 16th | 0.015 | 0.044 | 0.001 | 0.055 | 0.115 | |
| 17th | 0.013 | 0.053 | 0.036 | 0.062 | 0.132 | |
| 18th | 0.023 | 0.043 | 0.009 | 0.046 | 0.102 | |
| 19th | 0.017 | 0.054 | 0.03 | 0.071 | 0.118 | |
| 20th | 0.001 | 0.039 | 0.004 | 0.049 | 0.092 | |
| 21th | 0.014 | 0.058 | 0.029 | 0.073 | 0.107 | 0.160 |
| 22th | 0.004 | 0.035 | 0.035 | 0.042 | 0.084 | |
| 23th | 0.037 | 0.055 | 0.025 | 0.059 | 0.098 | 0.147 |
| 24th | 0.014 | 0.041 | 0.001 | 0.037 | 0.077 | |
| 25th | 0.02 | 0.039 | 0.023 | 0.048 | 0.090 | 0.135 |
| 26th | 0.006 | 0.028 | 0.016 | 0.035 | 0.071 | |
| 27th | 0.015 | 0.037 | 0.03 | 0.05 | 0.083 | 0.124 |
| 28th | 0.001 | 0.022 | 0.003 | 0.037 | 0.066 | |
| 29th | 0.02 | 0.038 | 0.011 | 0.051 | 0.078 | 0.117 |
| 30th | 0.007 | 0.028 | 0.012 | 0.026 | 0.061 | |
| 31th | 0.017 | 0.03 | 0.023 | 0.049 | 0.073 | 0.109 |
| 32th | 0.009 | 0.022 | 0.019 | 0.029 | 0.058 | |
| 33th | 0.016 | 0.04 | 0.01 | 0.041 | 0.068 | 0.102 |

| | | | | | | |
|------|-------|-------|-------|-------|-------|-------|
| 34th | 0.006 | 0.02 | 0.019 | 0.037 | 0.054 | |
| 35th | 0.014 | 0.033 | 0.015 | 0.047 | 0.064 | 0.096 |
| 36th | 0.01 | 0.026 | 0.007 | 0.026 | 0.051 | |
| 37th | 0.007 | 0.022 | 0.01 | 0.038 | 0.061 | 0.091 |
| 38th | 0.004 | 0.019 | 0.011 | 0.024 | 0.048 | |
| 39th | 0.008 | 0.074 | 0.015 | 0.09 | 0.058 | 0.087 |
| 40th | 0.009 | 0.018 | 0.012 | 0.025 | 0.046 | |

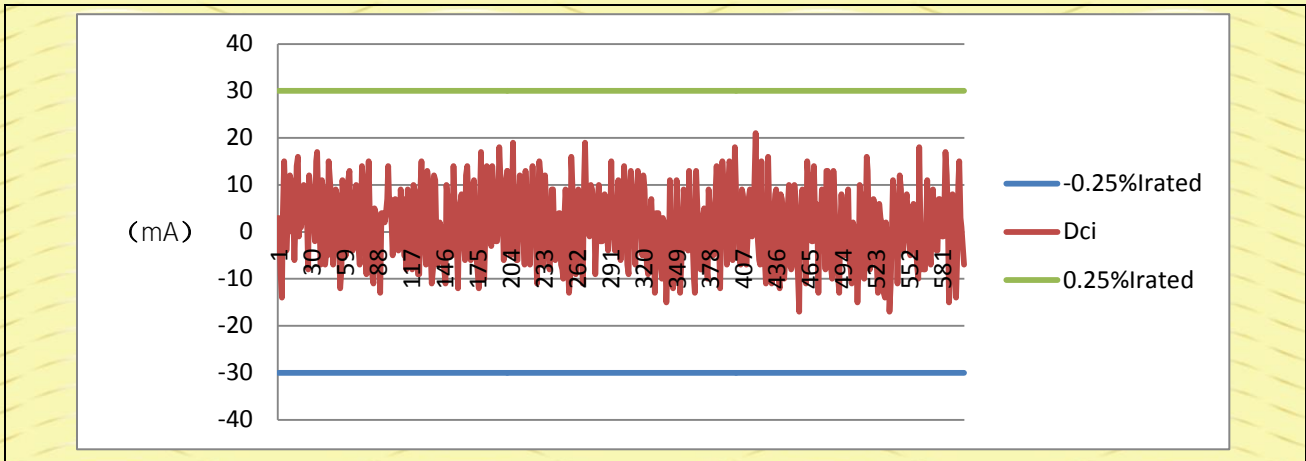
Note:

The higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

| | | | | | | | | | |
|---|------------------|-------|---------------|---------------|-------|---------------|------------------|------------------|--|
| Flicker | | | | | | | | P | |
| The requirement is specified in section 5.4.2, test procedure in Annex A or B 1.4.3 | | | | | | | | | |
| | Starting | | | Stopping | | | Running | | |
| | d_{max} | d_c | $d_{(t)}$ | d_{max} | d_c | $d_{(t)}$ | P_{st} | P_{It} 2 hours | |
| Measured values | 0.00 | 0.00 | 0.00 | 0.00 | 0.00 | 0.00 | 0.07 | 0.07 | |
| Normalised to standard impedance and 3.68kW for multiple units | 0.00 | 0.00 | 0.00 | 0.00 | 0.00 | 0.00 | 0.07 | 0.07 | |
| Limits set under BS EN 61000-3-2 | 4% | 3.3% | 3.3% 500ms | 4% | 3.3% | 3.3% 500ms | 1.0 | 0,65 | |
| Test start date | 2015.10.27 15:50 | | | Test end date | | | 2015.10.27 15:50 | | |

| | | | |
|---|--------|-------|-------|
| Power quality. DC injection | | | P |
| The requirement is specified in section 5.5, test procedure in Annex A or B 1.4.4 | | | |
| Test level power | 10% | 55% | 100% |
| Recorded value | 10mA | 4mA | 3mA |
| As % of rated AC current | 0.077% | 0.03% | 0.02% |
| Limit | 0.25% | 0.25% | 0.25% |

Diagramm of permanent DC-Injection



| | | | | | |
|---|--------|-------|-------|---|---|
| Power Quality. Power factor | | | | | P |
| The requirement is specified in section 5.6, test procedure in Annex A or B 1.4.2 | | | | | |
| | 216.2V | 230V | 253V | Measured at three voltage levels and at full output. Voltage to be maintained within $\pm 1.5\%$ of the stated level during the test. | |
| Measured value | 0.999 | 0.999 | 0.999 | | |
| Limit | >0.95 | >0.95 | >0.95 | | |

| Protection. Frequency test | | | | | | P |
|---|-----------|------------|-----------|------------|------------------|-----------------|
| The requirement is specified in section 5.3.1, test procedure in Annex A or B 1.3.3 | | | | | | |
| Function | Setting | | Trip test | | No trip test | |
| | Frequency | Time delay | Frequency | Time delay | Frequency / time | Confirm no trip |
| U/F stage 1 | 47.5Hz | 20s | 47.53 Hz | 20.25 s | 47.7Hz 25s | no trip |
| U/F stage 2 | 47Hz | 0.5s | 47.03 Hz | 816 ms | 47.2Hz 19.98s | no trip |
| | | | | | 46.8 Hz 0.48s | no trip |
| O/F stage 1 | 51.5Hz | 90s | 51.47 Hz | 90.3 s | 51.3Hz 95s | no trip |
| O/F stage 2 | 52Hz | 0.5s | 51.97 Hz | 886ms | 51.8Hz 89.98s | no trip |
| | | | | | 52.2Hz 0.48s | no trip |

| Protection. Voltage test | | | | | | P |
|---|---------|------------|-----------|------------|----------------|-----------------|
| The requirement is specified in section 5.3.1, test procedure in Annex A or B 1.3.2 | | | | | | |
| Function | Setting | | Trip test | | No trip test | |
| | Voltage | Time delay | Voltage | Time delay | Voltage / time | Confirm no trip |
| U/V stage 1 | 200.1V | 2.5s | 202.3 V | 2.93 s | 204.1V | no trip |

| | | | | | | |
|-------------|--------|------|---------|-------|-------------------|---------|
| | | | | | 3.5s | |
| U/V stage 2 | 184V | 0.5s | 185.6 V | 900ms | 188V 2.48s | no trip |
| | | | | | 180v 0.48 sec | no trip |
| O/V stage 1 | 262.2V | 1.0s | 261.2 V | 1.41s | 261.9V 1.41sec | no trip |
| O/V stage 2 | 273.7V | 0.5s | 270.7 V | 992ms | 269.7V 0.922s | no trip |
| | | | | | 277.7V 0.48s | no trip |

Protection. Loss of Mains test

inverters can be tested to BS EN 62116.

| | | | | | | |
|--------------------------|--------------|--------------|---------------|--------------|--------------|---------------|
| Test Power and imbalance | 33% -5% Q | 66% -5% Q | 100% -5% P | 33% +5% Q | 66% +5% Q | 100% +5% P |
| Trip time. Limit is 0.5s | 0.338 | 0.388 | 0.398 | 0.344 | 0.353 | 0.364 |

Protection. Frequency change, Stability test

The requirement is specified in section 5.3.3, test procedure in Annex A or B 1.3.6

P

| | Start Frequency | Change | End Frequency | Confirm no trip |
|--------------------------|-----------------|-------------|---------------|-----------------|
| Positive Vector Shift | 49.5Hz | +9 degrees | | no trip |
| Negative Vector Shift | 50.5Hz | - 9 degrees | | no trip |
| Positive Frequency drift | 49.5Hz | +0.19Hz/sec | 51.5Hz | no trip |
| Negative Frequency drift | 50.5Hz | -0.19Hz/sec | 47.5Hz | no trip |

Protection. Re-connection time

The requirement is specified in section 5.3.4 Automatic Reconnection, test procedure in Annex A or B 1.3.5

P

Test should prove that the reconnection sequence starts after a minimum delay of 20 seconds for restoration of voltage and frequency to within the stage 1 settings of table 1.

| Voltage | |
|--------------------|------------------------|
| Time delay setting | Measured delay time(s) |
| 20s | 43.1 s |
| Frequency | |
| Time delay setting | Measured delay time(s) |
| 20s | 42.9 s |

| | | | | |
|---|---|-----------------|-----------------|-----------------|
| | Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1. | | | |
| | At 266.2V | At 196.1V | At 47.4Hz | At 51.6Hz |
| Confirmation that the SSEG does not re-connect. | no reconnection | no reconnection | no reconnection | no reconnection |

| | | | | | |
|---|--------|-------|---------------------|-------|------------|
| Short circuit Current Contribution | | | | | P |
| The requirement is specified in section 5.7, test procedure in Annex A or B 1.4.6 | | | | | |
| For a directly coupled SSEG | | | For a Inverter SSEG | | |
| Parameter | Symbol | Value | Time after fault | Volts | Amps |
| Peak Short Circuit current | - | - | 20ms | 12.2V | 300mA |
| Initial Value of aperiodic current | - | - | 100ms | 10.1V | 250mA |
| Initial symmetrical short-circuit current* | - | - | 250ms | 11.5V | 420mA |
| Decaying (aperiodic) component of short circuit current* | - | - | 500ms | 12.5V | 350mA |
| Reactance/Resistance Ratio of source* | - | - | Time to trip | 53ms | In seconds |

| | |
|--|-----|
| Self Monitoring – Solid state Disconnection | N/A |
| The requirement is specified in section 5.3.1, No specified test requirements. | |
| Not applicable since electro-mechanical relays are used. | |

| |
|--|
| Additional comments |
| GW3000D-NS/GW3600-NS is similar to GW3600D-NS in circuit and construction except for output rating of current and power. The test result can refer to GW3600D-NS . |